Power Electronics Projects

I. POWER ELECTRONICS based CONVERTERS

1. A Dual-Buck–Boost AC/DC Converter for DC Nanogrid with Three Terminal Outputs. **(IEEE2017)**
2. A Fault-Tolerant Series-Resonant DC–DC Converter. **(IEEE2017)**
3. A Quasi-Resonant Current-Fed Converter With Minimum Switching Losses. **(IEEE2017)**
4. A Sensitivity-Improved PFM *LLC* Resonant Full-Bridge DC–DC Converter with *LC* Antiresonant Circuitry. **(IEEE2017)**
5. A Three-Level Space Vector Modulation Scheme for Paralleled Converters to Reduce Circulating Current and Common-Mode Voltage. **(IEEE2017)**
6. Cascaded High-Voltage-Gain Bidirectional Switched-Capacitor DC–DC Converters for Distributed Energy Resources Applications. **(IEEE2017)**
7. Digital Sensor less Current Mode Control Based on Charge Balance Principle and Dual Current Error Compensation for DC–DC Converters in DCM. **(IEEE2017)**
8. Dual-Bridge LLC Resonant Converter with Fixed-Frequency PWM Control for Wide Input Applications. **(IEEE2017)**
9. Feed-Forward-based Control in a DC–DC Converter of Asymmetric Multistage-Stacked Boost Architecture. **(IEEE2017)**
10. High Light-Load Efficiency Power Conversion Scheme Using Integrated Bidirectional Buck Converter for Paralleled Server Power Supplies. **(IEEE2017)**
11. High-Efficiency Asymmetric Forward-Fly back Converter for Wide Output Power Range. **(IEEE2017)**

12. Optimal Design of Complex Switched-Capacitor Converters via Energy-Flow-Path Analysis. **(IEEE2017)**
13. Control of Active Power Exchange With Auxiliary Power Loop in a Single-Phase Cascaded Multilevel Converter-Based Energy Storage System. **(IEEE2017)**

II. POWER ELECTRONICS based POWER FACTOR CORRECTION CONVERTER

1. A Single-Switch AC–DC LED Driver Based on a Boost-Fly back PFC Converter with Lossless Snubber. **(IEEE2017)**
2. Parameter Design of a Novel Series-Parallel-Resonant *LCL* Filter for Single-Phase Half-Bridge Active Power Filters. **(IEEE2017)**
3. A Method for the Suppression of Fluctuations in the Neutral-Point Potential of a Three-Level NPC Inverter with a Capacitor-Voltage Loop. **(IEEE2017)**

III. POWER ELECTRONICS based INVERTERS

1. A Unified Space Vector Pulse Width Modulation for Dual Two-level Inverter System. **(IEEE2017)**
2. Feedback Control Strategy to Eliminate the Input Current Harmonics of Matrix Converter under Unbalanced Input Voltages. **(IEEE2017)**
3. Maximum Boost Control of Diode-Assisted Buck–Boost Voltage-Source Inverter with Minimum Switching Frequency. **(IEEE2017)**
4. Model Predictive Control of Capacitor Voltage Balancing for Cascaded Modular DC–DC Converters. **(IEEE2017)**